

GUNNAMREDDY PHANEENDRA

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CAREER OBJECTIVE

Dedicated and detail-oriented **Embedded Systems Engineer** seeking to leverage my expertise in **firmware development, embedded programming, and communication protocols** to contribute effectively in a dynamic professional environment.

PROFESSIONAL EXPERIENCE

STUAM TECHNOLOGIES

Embedded Systems Engineer

November 2021 – Present

- Expertise in **embedded firmware development** and **microcontroller-based system design**.
- Experience with **communication protocols** including **USART, I2C, and SPI**.
- Proficient in **SPI Flash memory** and **Synthesizer technologies** such as **LX2694, ADMV8526, and LMX2694-TI**.
- Hands-on experience with **STM32 CUBE IDE, STM32 CUBE MX, and STM32 CUBE PROGRAMMER**.
- Skilled in **GUI integration via Ethernet** for embedded applications.
- Experience with **Simplicity Studio IDE (C8051F380)** and **SI5341/40 Clock Builder Pro**.

TECHNICAL SKILLS

- **Embedded Programming:** C, Embedded C
- **Communication Protocols:** USART, I2C, SPI
- **Microcontrollers:** STM32, C8051F380
- **Embedded Tools & IDEs:** STM32 CUBE IDE, **Keil IDE**, Simplicity Studio IDE, Clock Builder Pro
- **Hardware Integration:** Synthesizer (**LMX2694**), **SPI Flash**, **USB2ANY**
- **GUI & Ethernet Integration**

PROJECTS

Project 1: Global System for Mobile Communication (GSM Recorder RF Module)

Team Size: 4

Description: Designed and developed a **GSM RF module** with dual-channel frequency ranges (**1710-2700 MHz and 824-5000 MHz**) for signal processing and independent band selection.

Responsibilities:

- **GPIO management** for hardware control.
- **Attenuation control** for RF signal tuning.
- **GUI integration via Ethernet** for real-time data visualization.

Project 2: Radar Target Echo Simulator (RTS)

Team Size: 4

Description: Developed a **Radar Target Simulator** to simulate real-time target scenarios under

varying conditions within the **X-band frequency range (9-10 GHz)**.

Responsibilities:

- Integrated **LMX2694 Synthesizer** for frequency synthesis.
- **Interfaced FPGA with controller via SPI** for seamless data transfer.
- Developed configurations for **IF-IF, IF-RF, RF-IF, and RF-RF setups**.

Project 3: Digital Mobile Radio (DMR)

Team Size: 8

Description: Developed a **Digital Mobile Radio (DMR)** compliant with **ETSI Standard TS 102 361**, supporting both **voice and data communication** with enhanced security and efficiency features.

Responsibilities:

- Implemented **SPI Flash** for secure storage.
- Designed **alphanumeric & numeric keypad** for user input.
- Developed **password authentication mechanisms** for security.

EDUCATION

Bachelor of Technology (B.Tech) in Electronics & Communication Engineering
Jawaharlal Nehru Technological University, Anantapur (JNTUA), Andhra Pradesh

PERSONAL DETAILS

- **Date of Birth:** 04-10-1998
- **Father's Name:** G. Seshareddy
- **Mother's Name:** G. Aruna
- **Gender:** Male
- **Marital Status:** Single
- **Languages Known:** English, Telugu
- **Nationality:** Indian

DECLARATION

I hereby declare that the above-mentioned information is true to the best of my knowledge and belief.

Place: Hyderabad

Date:

(Gunnareddy Phaneendra)